Advanced Digital Design

Unit-I

**Assignments for Unit-I**

1. Install iverilog tools or Xilinx Vivado or any other similar tool and simulate the following designs: -
   1. Fibonacci number circuit
   2. Division circuit
   3. Period Counter
   4. Binary to BCD converter

All designs are available in the textbook: Pong P Chu, “FPGA Prototyping using Verilog Examples”